IN THE SPECIFICATION

Please replace the paragraph beginning on page 29, line 12 with the following paragraph:

The circuit 244 may be configured to generate the signal MODES in response to the signals A-M received from the circuit 242. In one example, the circuit 244 may be implemented as combinational logic (e.g., in an application specific integrated circuit or ASIC) or as a sequence of computer executable instructions (e.g., a software implementation). A circuit 244' is shown illustrating an example circuit 244 configured to logically combine the signals A-M to determine availability of intra 4 x 4 prediction modes 3, 7 and determine availability of intra 4 x 4 prediction modes 3, 7 and determine available to generate a control signal that enables modes 3 and 7 in response to either the samples A-H or the samples A-L being available. The circuit 244 may also be configured to generate a signal that enables mode 8 in response to the samples A-L or the samples I-L being available.